

Product Summary

V_R	650V
$I_{F(135/138^\circ\text{C})}$	10.5A/10A
Q_C	27.3nC



H3S065S010

H3S065K010

Features

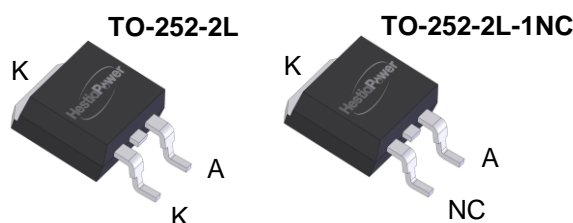
- Low Conduction and Switching Loss
- Zero Reverse Recovery
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient Device
- High Surge Current Capability
- RoHS Compliant and Halogen Free
- Suitable for High Power Application

Circuit Diagram



Benefits

- Higher System Efficiency
- Increase Parallel Device Convenience
- Enable High Temperature Application
- Allow High Frequency Operation
- Realize Compact and Lightweight Systems
- High Reliability



Applications

- Switching Mode Power Supply
- PFC
- UPS
- Motor Drives
- Flywheel diode in Power Inverters
- Solar/Wind Renewable Energy

Part Number	Package	Marking
H3S065S010	TO-252-2L	H3S065S010
H3S065K010	TO-252-2L-1NC	H3S065K010

Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	$I_R = 100 \mu\text{A}$	650	V
Peak Reverse Surge Voltage	V_{RSM}	$T_J = 25^\circ\text{C}$	650	V
DC Blocking Voltage	V_R	$T_J = 25^\circ\text{C}$	650	V
Continuous Forward Current	I_F	$T_c = 25^\circ\text{C}$	23.5	A
		$T_c = 135^\circ\text{C}$	10.5	
		$T_c = 138^\circ\text{C}$	10	
Non-Repetitive Peak Forward Surge Current	I_{FSM}	$T_c = 25^\circ\text{C}, T_p = 10 \text{ ms}, \text{Half Sine Wave}$	82	A
		$T_c = 125^\circ\text{C}, T_p = 10 \text{ ms}, \text{Half Sine Wave}$	72	
		$T_c = 25^\circ\text{C}, T_p = 10 \mu\text{s}, \text{Pulse}$	555	
Repetitive Peak Forward Surge Current	I_{FRM}	$T_c = 25^\circ\text{C}, T_p = 10 \text{ ms}$ Half Sine Wave, $D = 0.1$	47	A
		$T_c = 125^\circ\text{C}, T_p = 10 \text{ ms}$ Half Sine Wave, $D = 0.1$	36	
Power Dissipation	P_D	$T_c = 25^\circ\text{C}$	66.5	W
		$T_c = 125^\circ\text{C}$	22	
I^2t value	$\int i^2 dt$	$T_c = 25^\circ\text{C}, T_p = 10 \text{ ms}$	33	A^2s
Junction & Storage Temperature	T_J, T_{stg}		-55 to 175	$^\circ\text{C}$
Soldering Temperature	T_L		260	

Electrical Characteristics (T_c = 25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
DC Blocking Voltage	V _{DC}	I _R = 100 μA, T _J = 25°C	> 650			V
Forward Voltage	V _F	I _F = 10A, T _J = 25°C		1.42	1.7	V
		I _F = 10A, T _J = 175°C		1.63	1.9	V
Reverse Current	I _R	V _R = 650V, T _J = 25°C		1.5	75	μA
		V _R = 650V, T _J = 175°C		20	300	μA
Total Capacitive Charge	Q _C	I _F = 10A, di/dt = 300A/μs, V _R = 400V, T _J = 25°C		27.3		nC
Total Capacitance	C _j	V _R = 0.1V, T _J = 25°C, f = 1 MHz		576		
		V _R = 200V, T _J = 25°C, f = 1 MHz		57		pF
		V _R = 400V, T _J = 25°C, f = 1 MHz		48		
Capacitance Stored Energy	E _C	V _R = 400V		5.3		μJ

Thermal Resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance, Junction to Case	R _{θJC}		2.25		°C/W

Naming Rule

H3 S 065 S 010

Generation

H3 = 3rd Gen Discrete

Device Type

S = JBS diode (High Power) D = JBS diode (High Speed)

Breakdown Voltage

065 = 650V 120 = 1200V 170 = 1700V

Package Type

S = TO-252-2L K = TO-252-2L-1NC

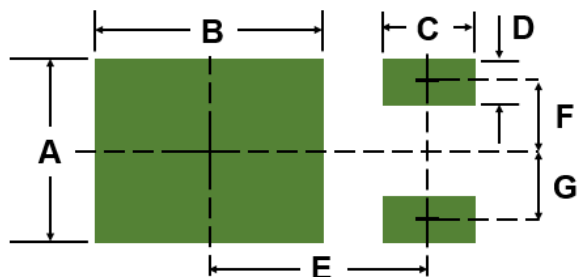
Typical Current Rating

002 = 2A 004 = 4A 006 = 6A 008 = 8A 010 = 10A 015 = 15A

Recommended Solder Pad Layout

TO-252-2L, TO-252-2L-1NC

Parameter	Symbol	Typical	Unit
Length	A	6.00	mm
	B	6.50	
	C	3.00	
	D	1.40	
	E	6.25	
	F	2.30	
	G	2.30	



Typical Device Performance

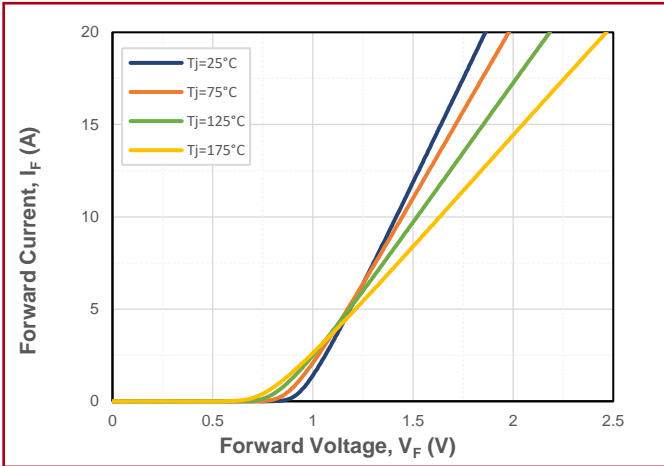


Fig.1 Forward Characteristics

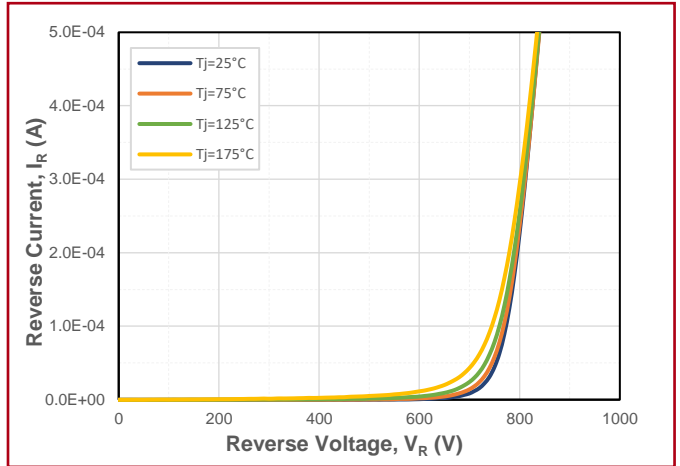


Fig.2 Reverse Characteristics

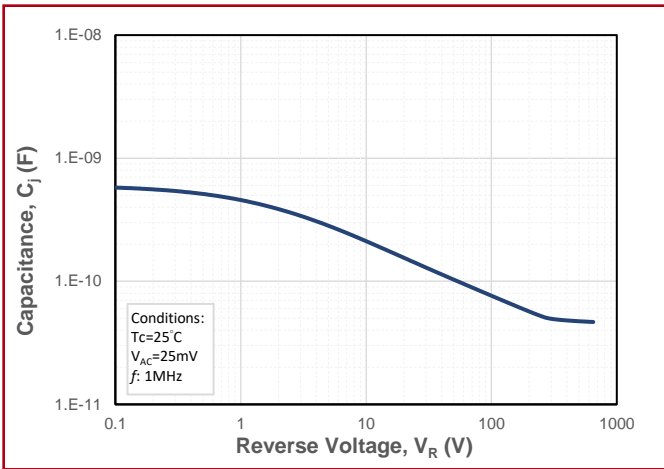


Fig.3 Junction Capacitance vs. Reverse Voltage

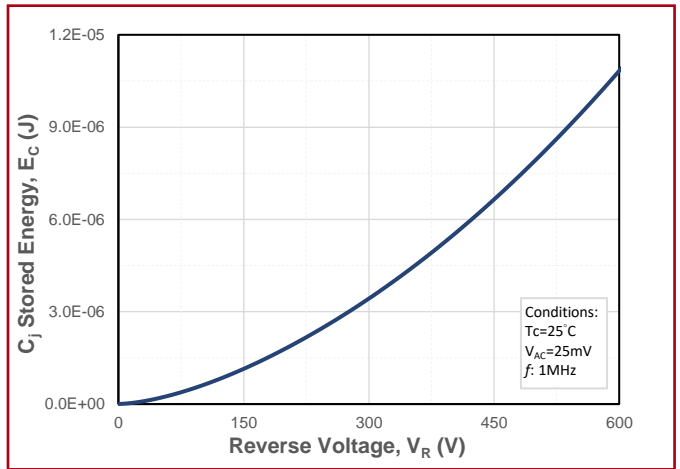


Fig.4 Capacitance Stored Energy

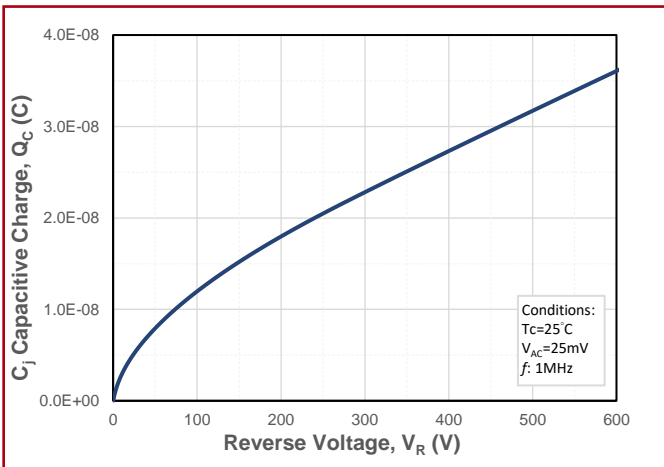
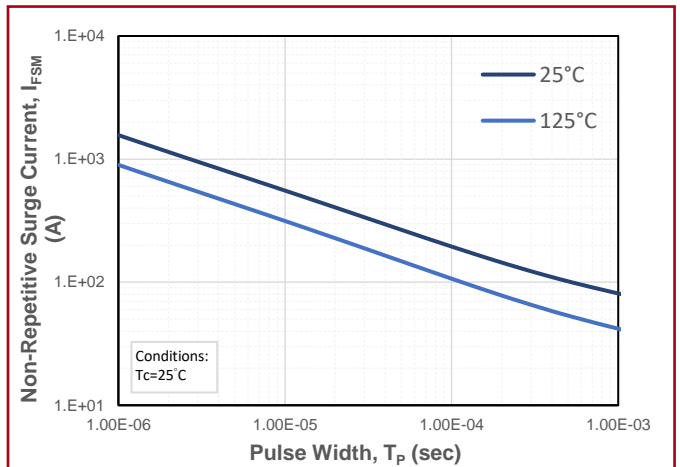


Fig.5 Recovery Charge vs. Reverse Voltage



The information provided herein is subject to change without notice.

Typical Device Performance

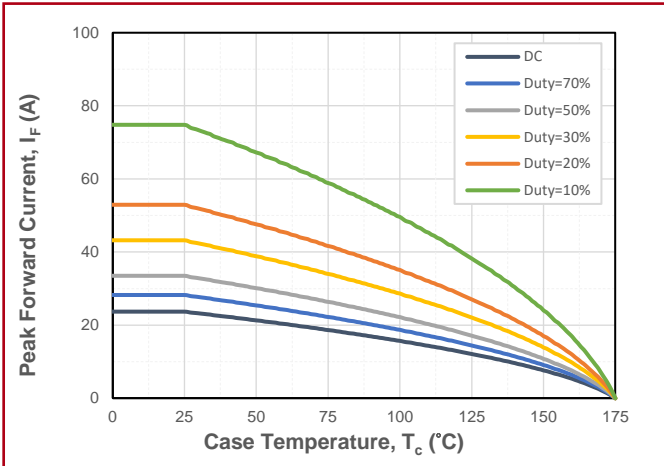


Fig.7 Maximum Forward Current Derating vs. Case Temperature

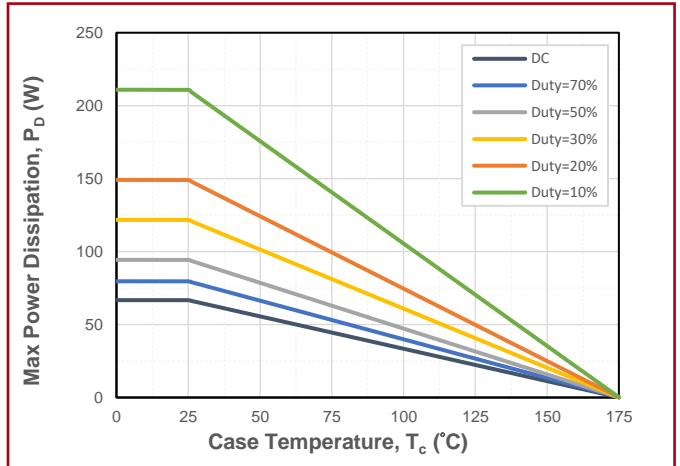


Fig.8 Maximum Power Dissipation Derating vs. Case Temperature

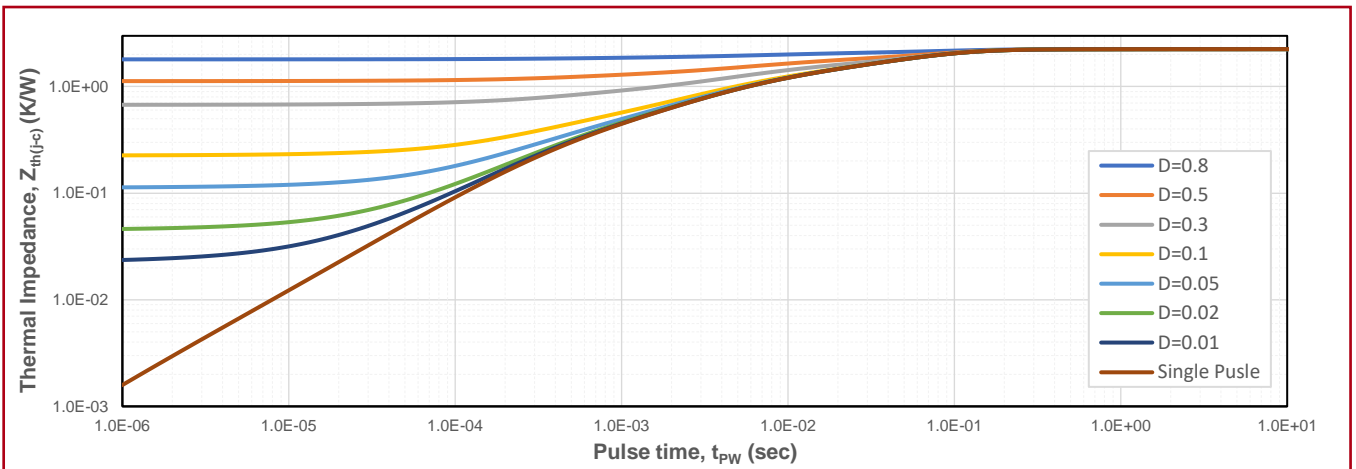
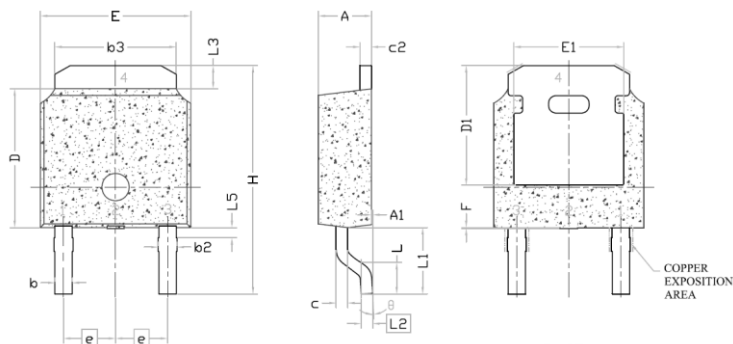


Fig.9 Transient Junction to Case Thermal Impedance

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Package Dimensions (TO-252-2L, TO-252-2L-1NC)



Symbol	mm		
	Min.	Typ.	Max.
E	6.40	6.60	6.731
L	1.40	1.52	1.77
L1	2.743 REF		
L2	0.508 BSC		
L3	0.89	--	1.27
L5	--	--	--
D	6.00	6.10	6.223
H	9.40	10.00	10.40
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e	2.286 BSC		
A	2.20	2.30	2.38
A1	0	--	0.127
c	0.46	0.50	0.60
c2	0.46	0.50	0.58
D1	5.21	--	--
E1	4.40	--	--
F	--	--	0.45
θ	0°	--	10°

Note:

1. All Dimension Are In mm.
2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
4. The Package Top May Be Smaller Than The Package Bottom.
5. Dimension "b" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.10 mm Total In Excess Of "b" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.